

# 10Gb/s 10Km CDR SFP+ 1310nm Transceivers

SFP+ 10Gb/s 10Km CDR SFP+ 1310nm Transceivers  
 1310nm, 10Gb/s, 10km, SFP+, CDR, SFP-8431, SFP-8432,  
 10GFC R 4.0, IEEE 802.3a 10GBASE-LR/LW,  
 T a GR-253-CORE OC-192 SR-1 a ITU-T  
 G.691 STM-64 I-64.1. T a a  
 CML a a a a a a a  
 IEEE 802.3a a a . T  
 a a a a CML a a a.D a  
 a a a a ab a a 2- a  
 a , a SFP-8472.

S a

(T =-5 C 70 C a V = 3.14 3.46V)

Parameter	Symbol	Unit	Min	Typ	Max	Note
T a						
N a Wa			1290		1330	
S M S Ra	SMSR	B	30			
S a W (-20 B)	$\Delta$				1	
O a O P	Pa	B	-6		-1	
E Ra	ER	B	6			
T a a D P a	TDP	B			1	1
A a La P OFF T a	POFF	B			-30	

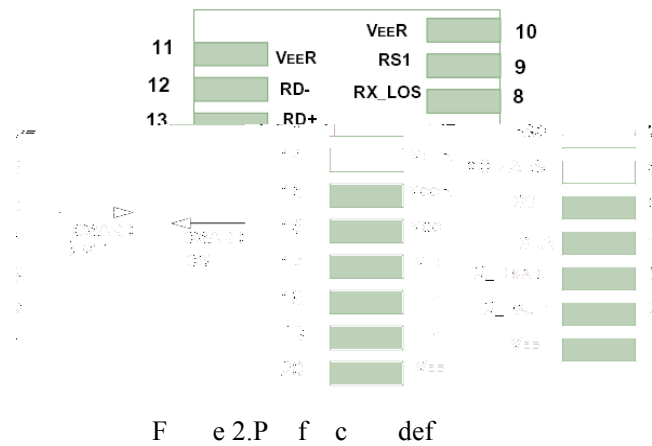
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## Electric Ports Definition

Pin Name	Symbol	Function
<b>Transmit</b>		
<b>Receive</b>		

## Pin function definitions



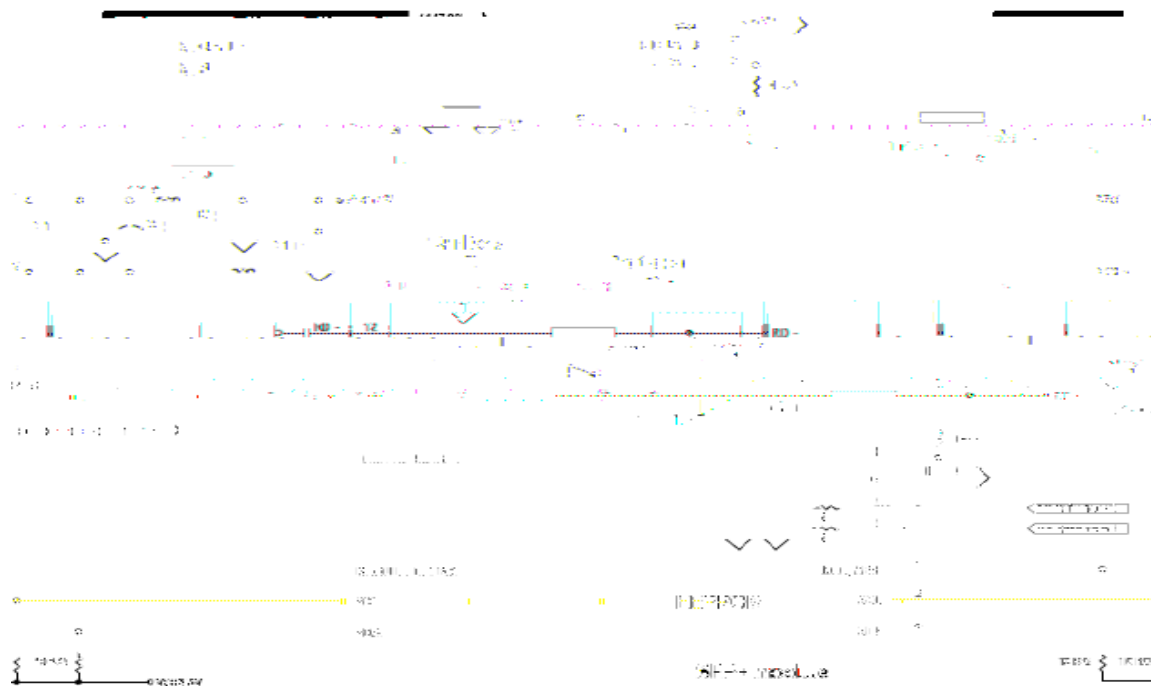
Pin Name	Symbol	Name	Description
1,17,20	VeeT	Transceiver Supply	Transceiver Supply
2	TXFa	Transceiver Output (OC)	Transceiver Output (OC) = Laser Fault (Laser Fault)

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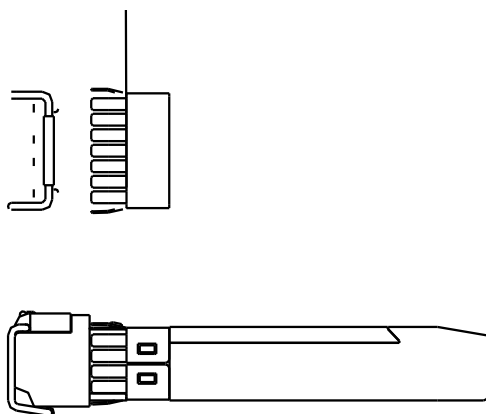
				T	e	c	e	c	a	b	e	, a	d	d	b	e	e	d
				H	V	c	c	a	10	!	e	.						
3	TX D ab e	T a e D ab e I (LVTTTL)		T	e	a	e	d	V	c	T	a	10	!				
4	SDA			S	e	a	I	D	S	F	F	8	4	7	2	D	a	c
5	SCL	M d e Def	I d e f e	M d e Def					d	b	e	e	d	H	V	c	c	
6	MOD-ABS			10	!	e	.											
7	RS0	Rece e Ra e Se ec (LVTTTL)		T	e	e	a	e	a	e	a	30	!	-d				d. A
9	RS1	T a e Ra e Se ec (LVTTTL)		a	e	e	f	e	e					affec				d e
8	LOS	L f S a O (OC)		S	f	c	e	c	a	a	f	e	a					
				BER < 1	0	10 <sup>-12</sup>	=	L	c	0								
				I	f	c	e	c	a	a	f	e	a					
				BER < 1	0	10 <sup>-12</sup>	=	L	c	1								
				T	e	c	e	c	a	b	e	, a	d	d	b	e	e	d
				H	V	c	c	a	10	!	e	.						
10,11,14	VeeR	Rece e S a G d		T	e	e		d	b	e	c	e	c	e	d	a	d	e
				b	a	d												
12	RD-	Rece e Ne a e DATA O (CML)		L	e	a	=	L	c	0	O	Rece e DATA						a 50!
				e	a	AC c	e	d	a	d	e	e	e	a	e			
				e	.													
13	RD+	Rece e P e DATA O (CML)		L	e	a	=	L	c	1	O	Rece e DATA						a 50!
				e	a	AC c	e	d	a	d	e	e	e	a	e			
				e	.													
15	VccR	Rece e P e S		T				d	b	e	c	e	c	e	d	a	f	e
				e				f	e									+3.3V e
																		See F e 3.Rec e ded
16	VccT	T a e P e S		T				d	b	e	c	e	c	e	d	a	f	e
				e				f	e									+3.3V e
																		See F e 3.Rec e ded
18	TD+	T a e P e DATA I (CML)		L	e	a	=	L	c	1	I	T a e DATA						a e
				100!	e	AC c	e	d	a	d	e	a	e	a	e			
					.													a d ffe e a
19	TD-	T a e Ne a e DATA I (CML)		L	e	a	=	L	c	0	I	T a e DATA						a e
				100!	e	AC c	e	d	a	d	e	a	e	a	e			
					.													a d ffe e a

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## Typical Application Circuit



## Package Outline



Unit: mm  
Unspecified Tolerance: ±0.1mm

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## Regulatory Compliance

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Fea e	Te Me hod	Pe fo mance
